## TRANSMITTAL

## Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention METHOD FOR OPTIMAL USE OF DIRECT FIT AND INTERPOLATED MODELS IN SCHEMATIC CUSTOM DESIGN OF ELECTRICAL CIRCUITS

Application Number :

Date

First Named Applicant: Joseph A. Iadanza

Confirmation Number:

Attorney Docket Number: BUR920030174US1

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Documents being submitted:	Files	
us-fee-sheet	BUR920030174US1-usfees.xml	
	us-fee-sheet.xsl	
	us-fee-sheet.dtd	
us-assignment	BUR920030174US1-usassn.xml	
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	BUR920030174US1Assignment1of2.tif	
	BUR920030174US1Assignment2of2.tif	
us-request	BUR920030174US1-usrequ.xml	
	us-request.dtd	
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us-declaration	BUR920030174US1Declaration1of1.tif	
application-body	BUR920030174US1spec-trans.xml	
	us-application-body.xsl	
	application-body.dtd	
	wipo.ent	
	mathml2.dtd	
	mathml2-qname-1.mod	
	isoamsa.ent	
	isoamsb.ent	
	isoamsc.ent	
	isoamsn.ent	
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	isoamsr.ent	
	isogrk3.ent	
	isomfrk.ent	
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	isotech.ent	
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	BUR920030174US1FIGSsheet1of3.tif	
	BUR920030174US1FIGSsheet2of3.tif	
	BUR920030174US1FIGSsheet3of3.tif	
Comments		
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